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MS AF
REPLY UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2184

PATENT
1248-0497P

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Technology Center 2100

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:	SHIGEMASA et al.	Conf.:	3009
Appl. No.:	09/552,701	Group:	2184
Filed:	April 19, 2000	Examiner:	E. C. PUENTE
For:	MICROCOMPUTER HAVING BUILT-IN NONVOLATILE MEMORY AND CHECK SYSTEM THEREOF AND IC CARD PACKING MICROCOMPUTER HAVING BUILT-IN NONVOLATILE MEMORY AND CHECK SYSTEM THEREOF		

REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116

MS AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 28, 2003

Sir:

In reply to the Office Action dated May 28, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application.